

**2022-23**  
**ES&VLSI**

**ACADEMIC YEAR 2022-23: ODD SEMESTER**

<b>FIRST SEMESTER TIMETABLE</b>	<b>1ES&amp;VLSI</b>
<b>M.Tech.ES &amp; VLSI</b>	

**Class Teacher:** Sri B. Jeevan

w.e.f. 5.12.2022

Day	09.10-10.00	10.00-10.50	10.50-11.40	11:40-12:30		13.30-14.20	14.20-15.10	15:10-16:00	16:00-16:50
MON	←-----MCES LAB-----→ KSJ			ESD B I-220	<b>L U N C H  B R E A K</b>	DICD B VI-207	SVDV B VI-207	RMIPR B VI 101	MCES(T) B VI-207
TUE	←-----DICD LAB-----→ BJ			MCES B I-220		EPTW(T) B I-220	SVDV(T) B I-220	LIBRARY	-
WED	←-----DICD LAB-----→ BJ			EPRW SH 3-5		MCES B I-220	←-----SEMINAR-----→		-
THU	EPRW SH 3-5	DICD B VI-310	SVDV B VI-310	ESD B VI-310		RMIPR B VI-103	DICD(T) B VI-310	LIBRARY	-
FRI	SVDV B I-220	ESD B VI-310	ESD(T) B VI-310	DICD B VI-310		MCES B VI-310	←-----MCES LAB-----→ KSJ		
SAT	-								

Course Title	Course Code & Course Name		Course Faculty
DICD	P20EV101: Digital IC Design	-	Sri B. Jeevan(BJ)
MCES	P20EV102: Microcontroller based Embedded Systems	-	Smt. K. Shailaja(KSJ)
SVDV	P20EV103B: System Verilog for Design and Verification	-	Smt. B. Neerajakshi(BN)
ESD	P20EV104A: Embedded System Design	-	Sri. B. Krishna Sundeep(BKS)
RMIPR	P20MC107: Research Methodology and IPR	-	Sri Dr. R.Srikanth(RS)
ERPW	P20AC108A: English for Research Paper Writing	-	Sri Dr. M.Om Prakash(MOP)
DICD LAB	P20EV105: Digital IC Design Laboratory	-	Sri B. Jeevan(BJ)
MCES LAB	P20EV106 : Microcontroller based Embedded Systems Laboratory	-	Smt. K. Shailaja(KSJ)

*Smithe*

Faculty I/c,  
Dept. Timetables

Faculty I/c,  
Overall Timetables

Head of the Department

Dean, AA

Principal